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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Louis Lippincott et al.

Art Unit:

2621

Serial No.:

10/750,075

Examiner:

Assignee:

Chikaodili E. Anyikire

Filed:

December 31, 2003

Docket:

ITL.1703US

P17498

For:

Motion Estimation Sum of all

Intel Corporation

Differences (SAD) Array Having Reduced Semiconductor Die

Area Consumption

Mail Stop AF Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed November 7, 2007, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made to the claims.